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OVERVIEW

The purpose of this document is to explain in detail how to perform the firmware update of the module EtherLoRa ETH-LORA-M-AX-01 (V1.2).

EtherLoRa is built with the integrated In Application Programming (IAP) bootloader to update the firmware without using a programmer adaptor. The latest update can be uploaded to EtherLoRa using this IAP bootloader via UART interface. Hence, the UART lines are recommended to have an external access in the final product design.

The bootloader uses the UART pins (RxD and TxD) and the power lines (VDD and GND) of the EtherLoRa.

This document is divided in three main parts:

- Hardware information
- Software information
- Instruction

REQUIREMENT

To perform the firmware update, the items below are needed.

Hardware Tools:
- EtherLoRa module ETH-LORA-M-AX-01 (V1.2).
- USB-UART Cable (FTDI USB-UART TTL Cable)
- Computer

Software Tools:
- Driver for USB-UART cable
- Ethertronics Update Firmware Tool
- Tera Term

SCOPE

This document focuses on the process to update the firmware of EtherLoRa using the built in IAP Bootloader Tool independently of the functionality of the module.

HARDWARE INFORMATION

ETHERLORA

Ethertronics' LoRa Module ETH-LORA-M-AX-01 (V1.2) is a SMT mounted cost efficient radio module that operates in the unlicensed 868 MHz band. It combines a LoRa™ transceiver SX1272 of Semtech Corporation with Ethertronics chipset and technologies to maximize link budget and RF performances.

![Figure 1: EtherLoRa ETH-LORA-M-AX-01 (V1.2) pin out](image1.png)
ETHERLORA PIN DESCRIPTION

<table>
<thead>
<tr>
<th>Pin Number</th>
<th>Pin Name</th>
<th>Pin Type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>GND</td>
<td>Supply</td>
<td>Ground Connection</td>
</tr>
<tr>
<td>2</td>
<td>SWCLK</td>
<td>Digital In/Out</td>
<td>Digital IO / JTCK / SWCLK</td>
</tr>
<tr>
<td>3</td>
<td>SWDIO</td>
<td>Digital In/Out</td>
<td>Digital IO / JTMS / SWDIO</td>
</tr>
<tr>
<td>4</td>
<td>VDD-SX</td>
<td>Supply</td>
<td>Supply Voltage for SX1272 part</td>
</tr>
<tr>
<td>5</td>
<td>VDD-SX</td>
<td>Supply</td>
<td>Supply Voltage for SX1272 part</td>
</tr>
<tr>
<td>6</td>
<td>GND</td>
<td>Supply</td>
<td>Ground Connection</td>
</tr>
<tr>
<td>7</td>
<td>nRESET</td>
<td>D In</td>
<td>nReset, internally pulled-up, internally filtered with capacitor</td>
</tr>
<tr>
<td>8</td>
<td>P5</td>
<td>Digital In/Out</td>
<td>Digital IO / USART1 CTS / MIPI DATA (See Table 2)</td>
</tr>
<tr>
<td>9</td>
<td>P6</td>
<td>Digital In/Out</td>
<td>Digital IO / USART1 RTS / MIPI CLK (See Table 2)</td>
</tr>
<tr>
<td>10</td>
<td>VDD-ST</td>
<td>Supply</td>
<td>Supply Voltage for ST part</td>
</tr>
<tr>
<td>11</td>
<td>GND</td>
<td>Supply</td>
<td>Ground Connection</td>
</tr>
<tr>
<td>12</td>
<td>P7</td>
<td>Digital In/Out</td>
<td>Digital IO / SPI2 MOSO</td>
</tr>
<tr>
<td>13</td>
<td>P8</td>
<td>Digital In/Out</td>
<td>Digital IO / SPI2 MOSI / MIPI DATA (See Table 2)</td>
</tr>
<tr>
<td>14</td>
<td>P9</td>
<td>Digital In/Out</td>
<td>Digital IO / SPI2 CLK / MIPI CLK (See Table 2)</td>
</tr>
<tr>
<td>15</td>
<td>P10</td>
<td>Digital In/Out</td>
<td>Digital IO / SPI2 NSS / MIPI VIO (See Table 2)</td>
</tr>
<tr>
<td>16</td>
<td>GND</td>
<td>Supply</td>
<td>Ground Connection</td>
</tr>
<tr>
<td>17</td>
<td>VDD-ST</td>
<td>Supply</td>
<td>Supply Voltage for ST part</td>
</tr>
<tr>
<td>18</td>
<td>RxD</td>
<td>Digital In/Out</td>
<td>Digital IO / USART1 RX</td>
</tr>
<tr>
<td>19</td>
<td>TxD</td>
<td>Digital In/Out</td>
<td>Digital IO / USART1 TX / MIPI VIO (See Table 2)</td>
</tr>
<tr>
<td>20</td>
<td>P11</td>
<td>Digital In/Out</td>
<td>Digital IO</td>
</tr>
<tr>
<td>21</td>
<td>P12</td>
<td>Digital In/Out</td>
<td>Digital IO / I2C1-SCL / Interface Selection (See Table 2)</td>
</tr>
<tr>
<td>22</td>
<td>GND</td>
<td>Supply</td>
<td>Ground Connection</td>
</tr>
<tr>
<td>23</td>
<td>P13</td>
<td>Digital In/Out</td>
<td>Digital IO / I2C1-SDA / Interface Selection (See Table 2)</td>
</tr>
<tr>
<td>24</td>
<td>P14</td>
<td>Digital In/Out</td>
<td>Digital IO / MIPI AS VDD / WakeUp Pin</td>
</tr>
<tr>
<td>25</td>
<td>NC</td>
<td></td>
<td></td>
</tr>
<tr>
<td>26</td>
<td>BOOT</td>
<td>D In</td>
<td>Bootloader Pin 0, internally pulled down</td>
</tr>
<tr>
<td>27</td>
<td>GND</td>
<td>Supply</td>
<td>Ground Connection</td>
</tr>
<tr>
<td>28</td>
<td>NC</td>
<td></td>
<td></td>
</tr>
<tr>
<td>29</td>
<td>NC</td>
<td></td>
<td></td>
</tr>
<tr>
<td>30</td>
<td>GND</td>
<td>Supply</td>
<td>Ground Connection</td>
</tr>
<tr>
<td>31</td>
<td>RF</td>
<td>RF In/Out</td>
<td>LoRa Antenna Port</td>
</tr>
<tr>
<td>32,33,34,35</td>
<td>GND</td>
<td>Supply</td>
<td>Ground Connection</td>
</tr>
</tbody>
</table>

Table 1

To perform the firmware update, only four pins are needed
1. Ground connection (GND)
2. Supply voltage (VDD)
3. UART line USART1 RX (RxD)
4. UART line USART1 TX (TxD)

USB UART CABLE

The transfer of the new firmware data is using UART interface and YMODEM protocol. To send the data to EtherLoRa module, the USB to TTL serial UART converter cable with virtual COM port driver is necessary. FTDI TTL-232RG-VSW3V3-WE or TTL-232RG-VREG3V3-WE is recommended for this purpose. The datasheet of this product can be found on the FTDI website.

Link: [http://www.ftdichip.com/Products/Cables/USBTTLSerial.htm](http://www.ftdichip.com/Products/Cables/USBTTLSerial.htm), FTDI USB TTL UART Cables

Since this cable is wire ended, user needs to make the connectors to connect with the EtherLoRa module pins.
FTDI GENERIC CABLES SIGNAL DESCRIPTIONS

<table>
<thead>
<tr>
<th>Color</th>
<th>Name</th>
<th>Type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Black</td>
<td>GND</td>
<td>GND</td>
<td>Device ground supply pin.</td>
</tr>
<tr>
<td>Brown</td>
<td>CTS</td>
<td>Input</td>
<td>Clear to Send Control input / Handshake signal.</td>
</tr>
<tr>
<td>Red</td>
<td>VCC</td>
<td>Output</td>
<td>Power Supply Output</td>
</tr>
<tr>
<td>Orange</td>
<td>TXD</td>
<td>Output</td>
<td>Transmit Asynchronous Data output</td>
</tr>
<tr>
<td>Yellow</td>
<td>RXD</td>
<td>Input</td>
<td>Receive Asynchronous Data input</td>
</tr>
<tr>
<td>Green</td>
<td>RTS</td>
<td>Output</td>
<td>Request To Send Control Output / Handshake signal</td>
</tr>
</tbody>
</table>

Table 2

COMPUTER

The software tool is developed to be used on the computer that runs on windows operating system. Otherwise user can use the open source serial terminal such as Tera Term to perform the upgrade. (See next chapter 4.2)

CONNECTION

The connection of the four pins between the FTDI USB-UART cable and the EtherLoRa module is as below:

USB-UART CABLE AND ETHERLORA CONNECTION

<table>
<thead>
<tr>
<th>Color</th>
<th>Description</th>
<th>Pin no</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Red</td>
<td>Voltage Supply Output (VDD)</td>
<td>10 or 17*</td>
<td>Voltage Supply Input (VDD)</td>
</tr>
<tr>
<td>Black</td>
<td>Device ground supply (GND)</td>
<td>1, 6, 11, 16, 27, 30 or 32*</td>
<td>Ground Connection (GND)</td>
</tr>
<tr>
<td>Orange</td>
<td>Transmit UART Data output</td>
<td>18</td>
<td>USART1 RX Input</td>
</tr>
<tr>
<td>Yellow</td>
<td>Receive UART Data input</td>
<td>19</td>
<td>USART1 TX Output</td>
</tr>
</tbody>
</table>

Table 3

Note 1: Pin 10 and 17 for VDD are connected internally, user can choose either one of them to connect with RED wire of FTDI. In the picture below Pin 17 is used.

Note 2: Pin 1, 6, 11, 16, 27, 30 and 32 for GND are connected internally, user can choose either one of them to connect with BLACK wire of FTDI. In the picture below Pin 16 is used.
SOFTWARE INFORMATION

Two software products are needed for the update processes which are VCP driver for USB-UART Cable and EtherLoRa Update Firmware Tool or Tera Term. This document won't discuss the VCP driver in details. The VCP driver can be found on FTDI website as well as the instruction to install it.

Link: http://www.ftdichip.com/Drivers/VCP.htm, VCP Driver for FTDI USB-UART Cable

User can download EtherLoRa Update Firmware Tool installer from Ethertronics website or contact the developers from Ethertronics.

Link: www.avx.com/products/modules/lora-module, LoRa Update Firmware Tool’s Installer download

For the Tera Term downloading, user can find various download links on the internet.

Link: https://osdn.net/projects/tssh2/releases/, Tera Term Installer download

INSTRUCTION

ETHERLORA MODULE UPDATING FIRMWARE TOOL

Using the integrated bootloader (IAP) and the EtherLoRa Update Firmware Tool, the firmware of the module can be updated in the most convivial way.

- Download the VCP Driver of the FTDI USB-UART cable and install the driver.
- Download the installer of the EtherLoRa Update Firmware Tool and run the installer.
- Connect the FTDI USB-UART Cable and the EtherLoRa module. See details in Section 2.4 Connection.
- Connect the FTDI USB-UART Cable to the PC host and verify the COM Port:
  - Right click Computer ➔ Properties ➔ Device manager

DEVICE MANAGER INTERFACE

- Run the EtherLoRa Update Firmware Tool, Select the right COM port and click Initialize Button.

ETHERLORA UPDATE FIRMWARE TOOL - INITIALIZATION
Load the new firmware binary file.
Select if you want to update factory parameters came along with the Firmware file or not.
Click the button Upload to EtherLoRa, the progress bar will show the progression of the uploading based on size of the firmware.
In case of wrong image file, click cancel to abort the ongoing transfer, otherwise will until the end of the transfer.

TRANSFER IN PROGRESS

Once the transfer is completed, user can verify the version of the firmware using the AT command panel.
“ATI” shows the information of the firmware.

VERIFICATION OF THE FIRMWARE VERSION

TERA TERM

An alternative tool to perform the upgrade is to use open source serial terminal such as Tera Term. Once the connection is done, user can run the Tera Term application and the connection windows will be displayed automatically. Otherwise, user can open manually the new connection:

File ➔ New Connection ➔ Select the correct COM Port

TERA TERM COM PORT SELECTION
User needs to change the parameters of the Serial COM port as follows:

- File ➔ Serial port... ➔ adjust the parameters

TERA TERM SERIAL PORT SETUP

User can also adjust the setup of the terminal to display the answer correctly

- Setup ➔ Terminal... ➔ adjust New line receive to LF the terminal setup

TERA TERM TERMINAL SETUP

When everything is set up, user can start sending AT command to perform the upgrade, `AT#FLASH`, the menu will be displayed.

UPGRADE MENU
User must type ‘1’, then use the YMODEM transfer feature of the Tera Term.

- File ➔ Transfer ➔ YMODEM ➔ Send

TERA TERM YMODEM TRANSFER

Open file dialog will be popped up, and user needs to locate the updated firmware binary file and click open. User must locate the binary file within 2 minutes, otherwise the timeout will occur, and the module will reboot to the latest available firmware. Once user click open, the transfer should start.

TERA TERM YMODEM TRANSFER PROGRESS WINDOW

If there is any problem, please retry again. If the problem persists, please contact Ethertronics for the support.
LIST OF ABBREVIATIONS
IAP: In Application Programming
UART: Universal Asynchronous Receiver/Transmitter
USART: Universal Synchronous/Asynchronous Receiver/Transmitter
USB: Universal Serial Bus
TTL: Transistor–transistor logic level
SMT: Surface-Mount Technology
LoRa: Long range
OS: Operating System
VCP: Virtual COM Port
COM: Communication port

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Firmware Update for ETH-LORA-M-AX-01 (V1.2)

LIST OF ABBREVIATIONS
IAP: In Application Programming
UART: Universal Asynchronous Receiver/Transmitter
USART: Universal Synchronous/Asynchronous Receiver/Transmitter
USB: Universal Serial Bus
TTL: Transistor–transistor logic level
SMT: Surface-Mount Technology
LoRa: Long range
OS: Operating System
VCP: Virtual COM Port
COM: Communication port

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